

L Number	Hits	Search Text	DB	Time stamp
6	86	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic	USPAT	2003/12/09 16:58
7	83	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic	USPAT	2003/12/09 16:48
8	83	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic) and (transferadj start\$6)	USPAT	2003/12/09 16:48
9	14	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic) and (transfer adj start\$6)	USPAT	2003/12/09 17:54
10	0	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic) and reptition	USPAT	2003/12/09 16:54
11	36	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic) and repetition	USPAT	2003/12/09 16:54
12	440	(dma and ram and (address adj count\$5)) and (interrupt\$1 and register\$1 and (start\$6 adj address) and (arithmetic adj\$4 logic adj\$4 control\$7))	USPAT	2003/12/09 17:02
13	147	((dma and ram and (address adj count\$5)) and (interrupt\$1 and register\$1 and (start\$6 adj address) and (arithmetic adj\$4 logic adj\$4 control\$7))) and 710/\$.ccls.	USPAT	2003/12/09 17:38
14	16	((dma and ram and (address adj count\$5)) and (interrupt\$1 and register\$1 and (start\$6 adj address) and (arithmetic adj\$4 logic adj\$4 control\$7))) and 710/\$.ccls.) and (dma adj interrupt\$1)	USPAT	2003/12/09 17:38
15	10	("4067059" "4245305" "4400772" "4403282" "4455620" "4481572" "4481578" "4514808" "4543629" "4558412").PN.	USPAT	2003/12/09 17:49
16	5	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)and arithmetic) and logic) and ((interrupt\$7) adj (conter\$1 or queue\$1 or stor\$5))	USPAT	2003/12/09 17:55
17	14	("4103328" "4246637" "4268906" "4271468" "4296466" "4336588" "4514823" "4587609" "4591973" "4602327" "4682304" "4688166" "4901232" "5031091").PN.	USPAT	2003/12/09 18:16

L Number	Hits	Search Text	DB	Time stamp
-	562	dma and ram and (address adj count\$5) and interrupts	USPAT	2003/12/08 19:55
-	555	(dma and ram and (address adj count\$5) and interrupts) and register\$1	USPAT	2003/12/08 19:56
-	358	((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)	USPAT	2003/12/08 19:57
-	288	((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined	USPAT	2003/12/08 19:57
-	171	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary	USPAT	2003/12/08 20:16
-	8	("Re34850" "5001624" "5005121" "5237667" "5499348" "5504916" "5535417" "5634076").PN.	USPAT	2003/12/08 20:10
-	23	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and dmac	USPAT	2003/12/08 20:18
-	98	(((((dma and ram and (address adj count\$5) and interrupts) and register\$1) and (start\$6 adj address)) and predetermined) and temporary) and (signal adj process\$7)	USPAT	2003/12/09 16:46